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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of :
Hirofumi HARADA :
Serial No. 09/872,798 : Group Art Unit: 2814
Filed: June 1, 2001 : Examiner: Thao X. Le
For: VERTICAL MOS TRANSISTOR :
AND A METHOD OF :
MANUFACTURING THE SAME : Docket No. S004-4310(RCE)

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MS NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

TECHNIQUE 2803

RESPONSE

S I R:

In response to the Office Action mailed May 30, 2003, applicant amends his application as follows:

ADDITIONAL FEES:

No additional fees are believed required in connection with this response; however, should it be determined that a fee is due, authorization is hereby given to charge any such fee to our Deposit Account No. 01-0268.